L Number	Hits	Search Text	DB	Time stamp
-	8491	(lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/26 08:53
-	782	((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)) and (one adj chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/12/01 07:34
-	9	((((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)) and (one adj chip)) and debug) and @ad<20000710	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/26 08:54
-	19	(((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)) and (one adj chip)) and debug	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/26 14:37
-	19	(((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessor) and ((peripheral or auxilliary) adj circuit)) and (one adj chip)) and debug	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/26 13:52
-	1	09/900945	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/26 13:52
-	123	(((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit))) and debug	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/26 15:02
-	80	((((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit))) and debug) and @ad<20000710	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/26 14:38
-	136	(((lsi or(logic adj circuit) or (integrated adj circuit) or ic or MPU or microprocessror or processor) and ((peripheral or auxilliary) adj circuit))) and debug	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/26 15:04
-	90	((((lsi or(logic adj circuit) or (integrated adj circuit) or ic or MPU or microprocessror or processor) and ((peripheral or auxilliary) adj circuit))) and debug) and @ad<20000710	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/26 15:19
-	347	((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)) and (memory or DRAM or array)and (debug or lbist or test).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/12/01 07:35
-	241	(((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)) and (memory or DRAM or array)and (debug or lbist or test).ab.) and @ad<20000710	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/12/01 07:36